

09/147574

2N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 017835/0342

In re patent application of

Bruno FRANCOEUR et al.

Group Art Unit: Unknown

Serial No. 09/147,574

Examiner: Unknown

Filed: January 22, 1999

For: LOW STRAY INTERCONNECTION INDUCTANCE POWER CONVERTING MODULE FOR
CONVERTING A DC VOLTAGE INTO AN AC VOLTAGE, AND A METHOD THEREFORTRANSMITTALAssistant Commissioner for Patents
Washington, D.C. 20231

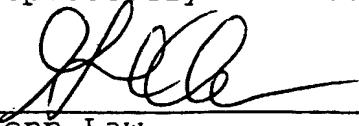
Sir:

Transmitted herewith is a Second Preliminary Amendment in the above-captioned application. The fee has been calculated as shown below. (Small entity fees indicated in parentheses.)

CLAIMS AS AMENDED						
(1)	(2)	(3)	(4)	(5)	(6)	(7)
	Claims Remaining After Amendment		Highest Number Previously Paid For	Extra Claims	Rate	Fee
Total Claims	54	-	20	34	18.00	612.00
(Small Entity)					(9.00)	
Independent claims	2	-	3	0	78.00	0.00
(Small Entity)					(39.00)	
Multiple Dependent					260.00	260.00
(Small Entity)					(130.00)	
Extension of Time	One Month		Two Months	Three Months	Four Months	Five Months
Fee	\$110		\$380	\$870	\$1360	\$1850
(Small Entity)	(\$55)		(\$190)	(\$435)	(\$680)	(\$925)
Total						\$872.00

A check in the amount of the above Total Fee is attached. This amount is believed to be correct; however, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 19-0741.

Respectfully submitted,


 Glenn Law
 Reg. No. 34,371

 Date: April 6, 1999
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Bruno FRANCOEUR *et al.*

Serial No. 09/147,574

Examiner: Unassigned

Filed: January 22, 1999

For: LOW STRAY INTERCONNECTION INDUCTANCE POWER
CONVERTING MODULE FOR CONVERTING A DC VOLTAGE
INTO AN AC VOLTAGE, AND A METHOD THEREFOR

SECOND PRELIMINARY AMENDMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 5 to 13, and add the following new claims 16-31.

--16. A power converting module according to claim 3, further comprising drivers mounted on the base nearby the power semiconductor devices, for driving the power semiconductor devices.

17. A power converting module according to claim 4, wherein said semiconductor devices are gate capacitance controlled semiconductor devices, and wherein each of the drivers includes:

a terminal to receive a gate signal for the corresponding semiconductor device;

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